

ABSTRACT OF THE DISCLOSURE

Circuit for generating a spread spectrum clock (CGSSC) that employs a mechanism to modulate the input voltage ( $V_{ctrl}$ ) to the VCO to achieve dithering. The CGSSC includes a voltage controller oscillator (VCO) that  
5 generates an output signal ( $F_{out}$ ). The VCO includes an input coupled to a voltage control node for receiving a voltage signal and an output for generating a clock signal that has a frequency dependent on the received voltage signal. A VCO input voltage modulation mechanism (VIVMM) is coupled to the voltage control node ( $V_{ctrl}$ ) for modulating or adjusting the voltage at the VCO input  
10 voltage node in a controlled manner to generate a spread spectrum clock.